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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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oplication of: Rangarajan, et al.

Serial No: 10/052,042

Filed: January 17, 2002

For: SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR MAPPING SYSTEM MEMORY IN A MULTIPLE NODE INFORMATION HANDLING SYSTEM

Group Art Unit: 2818

Examiner: Unknown

APR 25 2002
FECHNOLOGY CENTER 2800

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Dear Sir:

In compliance with the duty of disclosure under 37 CFR §1.56, and in accordance with the practice under 37 CFR §1.97 and §1.98, the Examiner's attention is directed to the documents listed on the enclosed modified Form PTO-1449. No inference should be made that the cited references are in fact material, are in fact prior art, or that no better art exists. The cited patents are listed in numerical order and are not in any order based on their pertinence. Copies of the listed information are attached.

This Information Disclosure Statement is being filed within three months of the United States filing date or before the mailing date of a first Office Action on the merits. No certification or fee is required (37 CFR §1.97(b)).

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

Respectfully submitted,

James R. Bell

Reg. No. 26,528

Facsimile: 512/867-8470

A-129860.1

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner For Patents, Washington, D.C.

20231 on 4/18/02

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